

Fig.1A

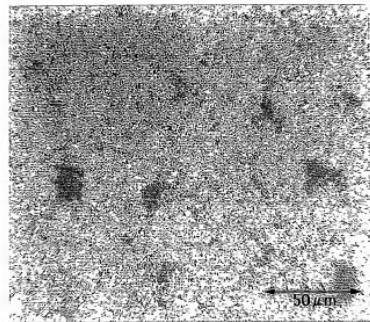


Fig.1B

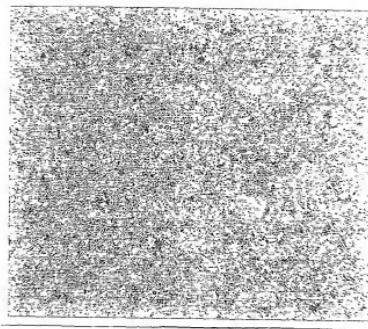


Fig.1C

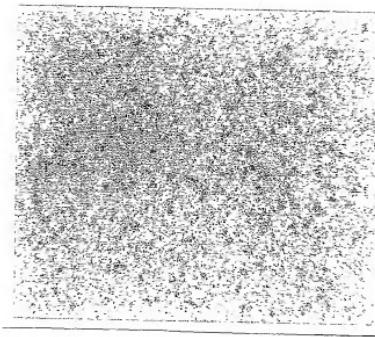


Fig.2A

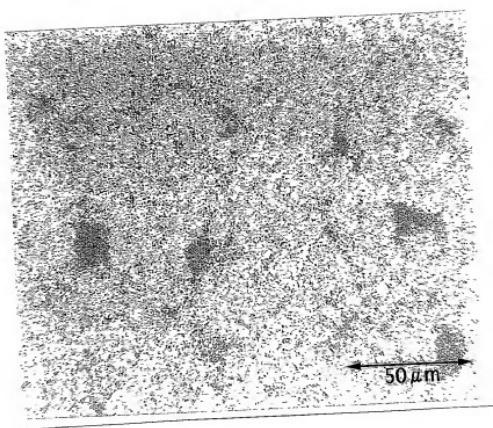


Fig.2B

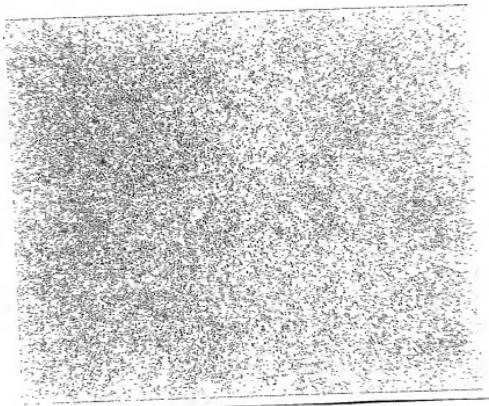


Fig.3A

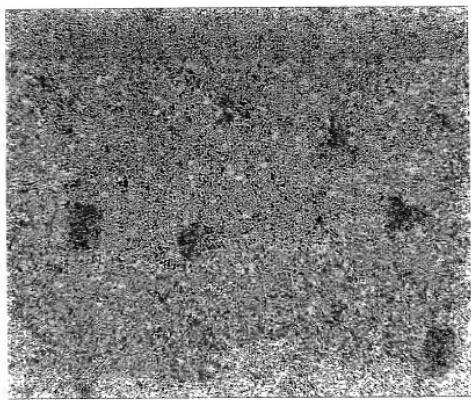


Fig.3B

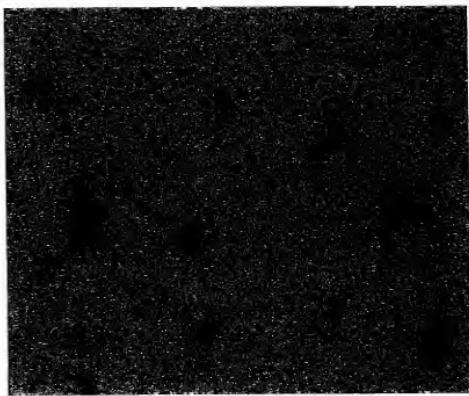


Fig. 4

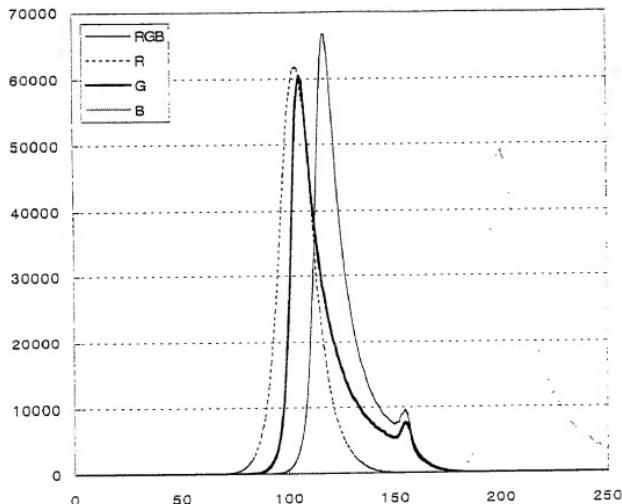


Fig.5A

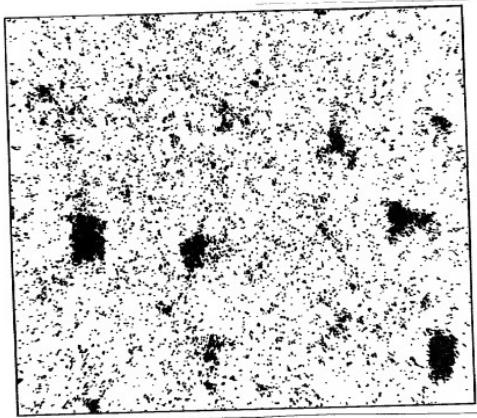


Fig.5B

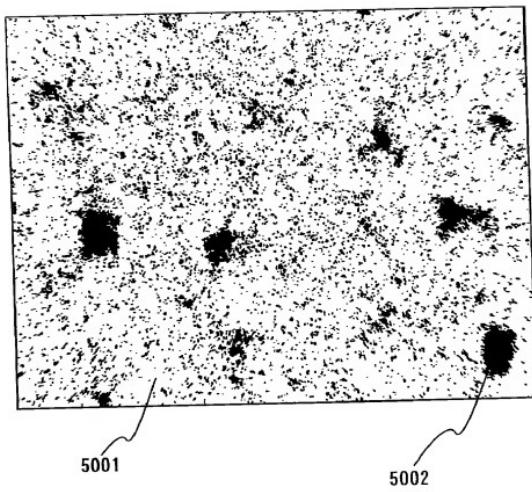


Fig. 6A

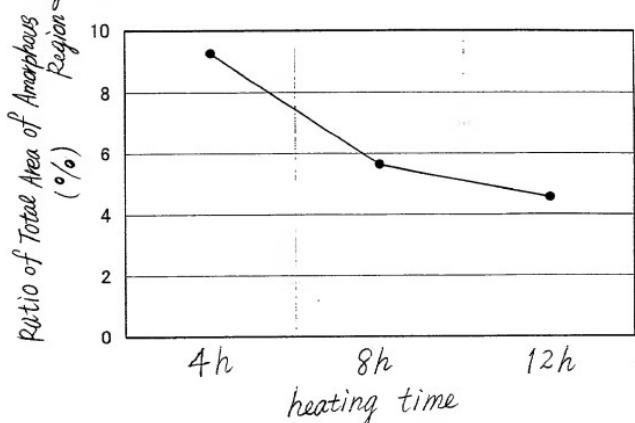


Fig. 6B

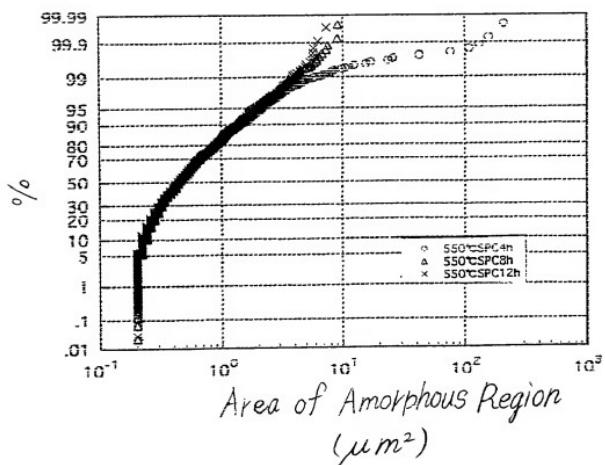


Fig. 7A

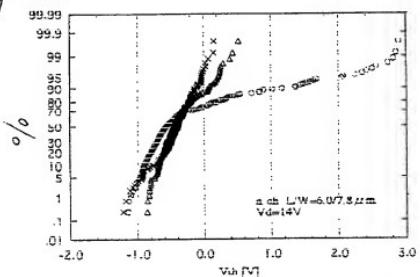


Fig. 7B

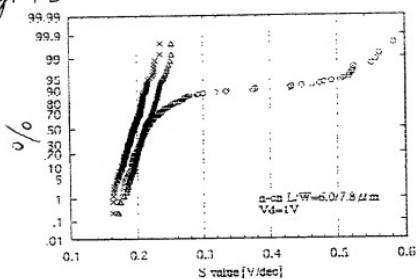


Fig. 7C

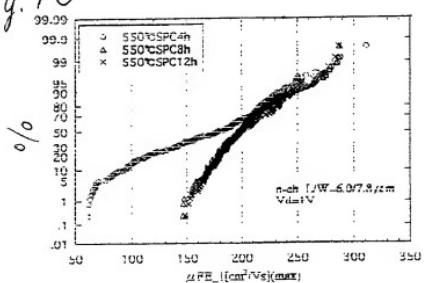


Fig.8A

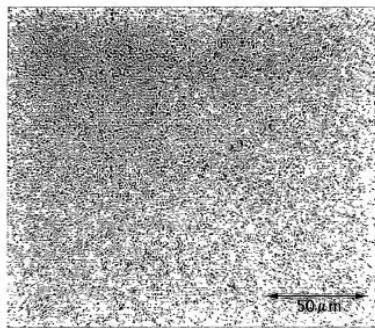


Fig.8B

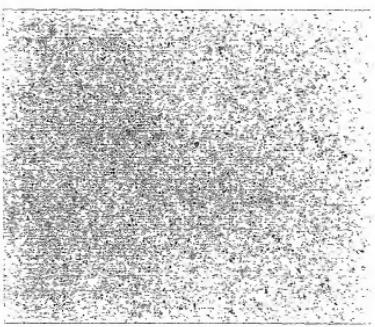
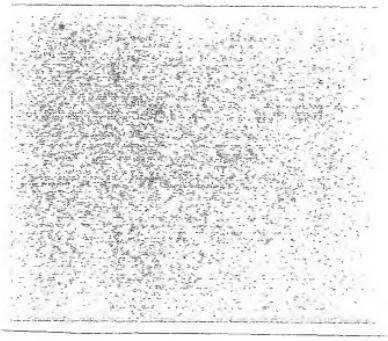


Fig.8C



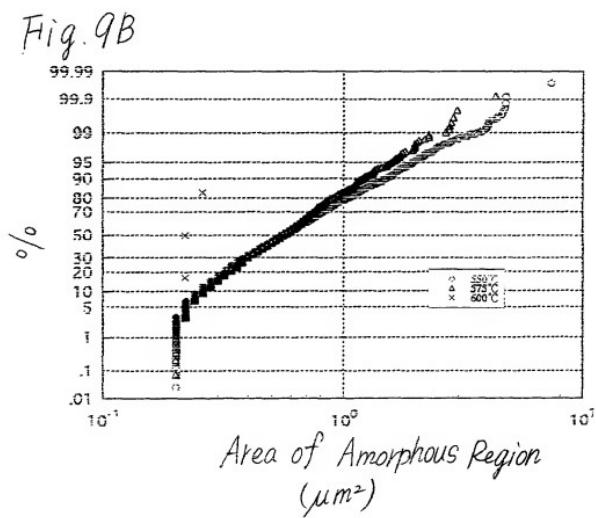
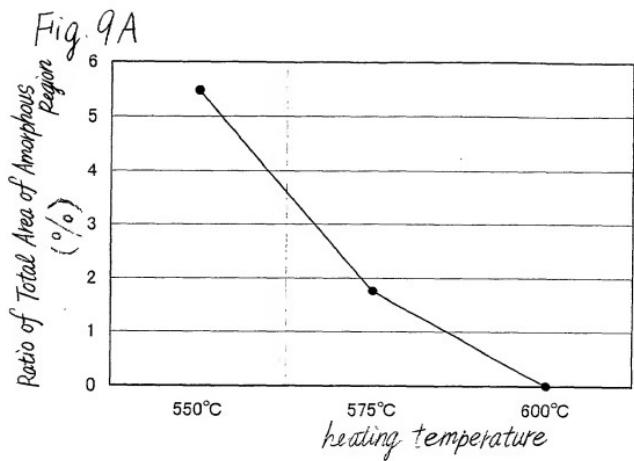


Fig.10A

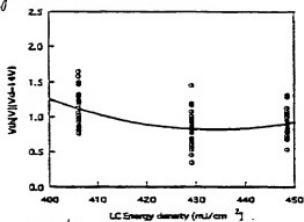


Fig.10E

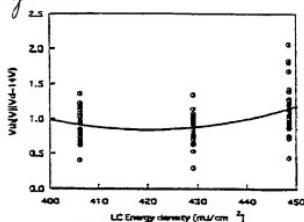


Fig.10B

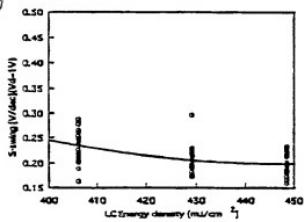


Fig.10F

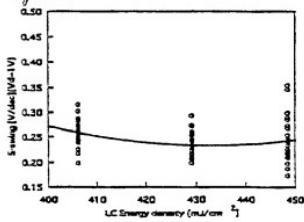


Fig.10C

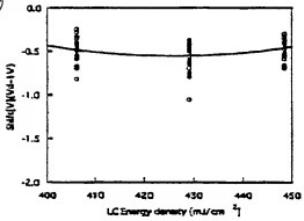


Fig.10G

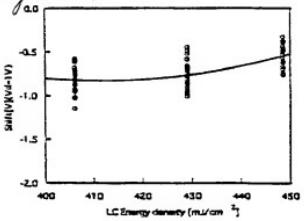


Fig.10D

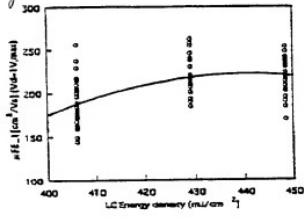


Fig.10H

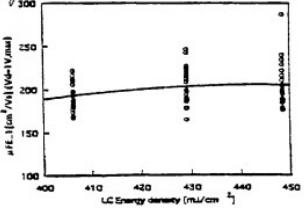


Fig. 11A

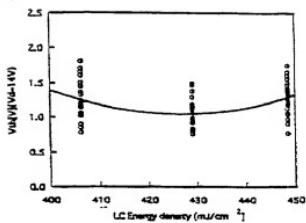


Fig. 11B

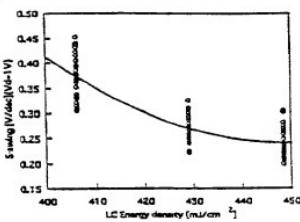


Fig. 11C

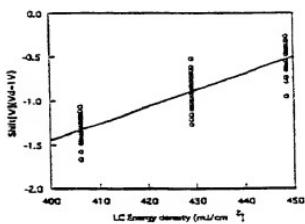


Fig. 11D

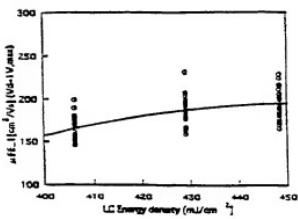


Fig. 12

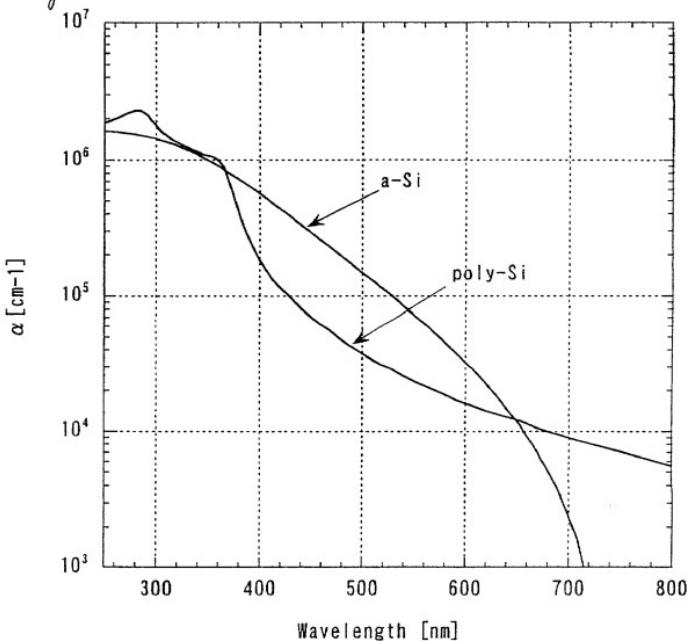
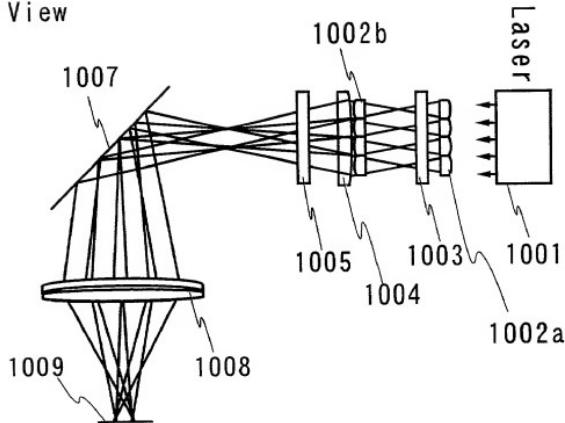


Fig.13

Side View



Top View

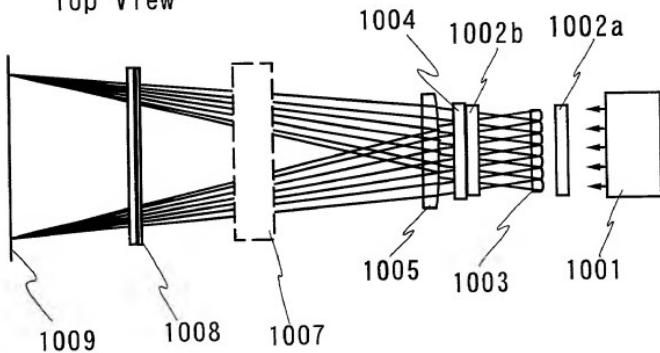


Fig.14

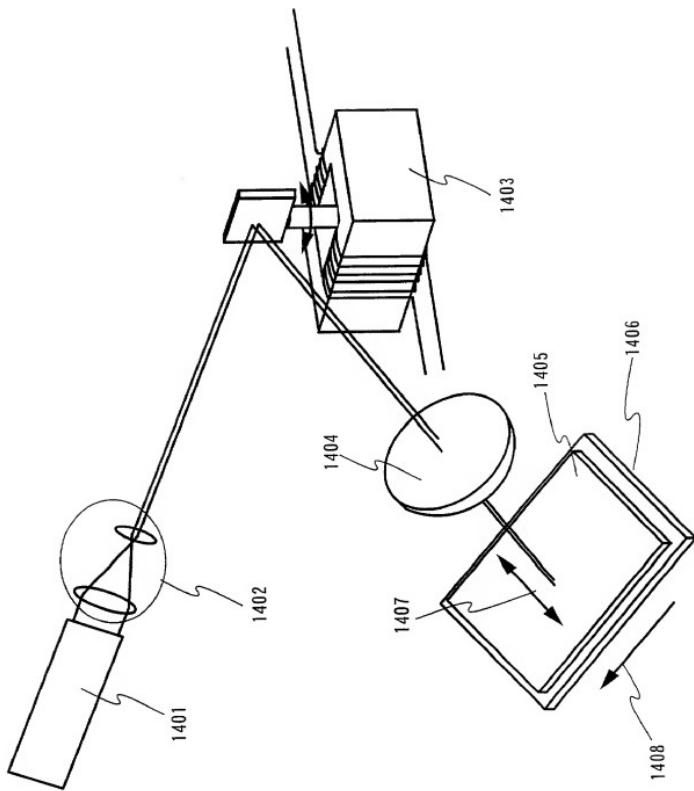


Fig.15A

101  
102b  
102a



Fig.15B

103b

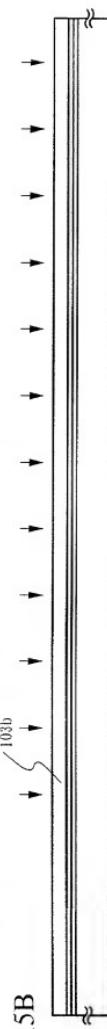


Fig.15C

109a  
P(M)

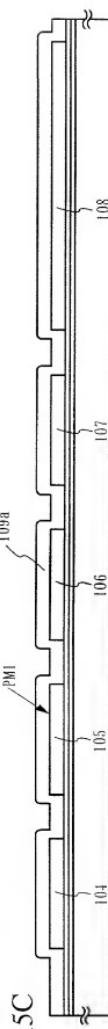
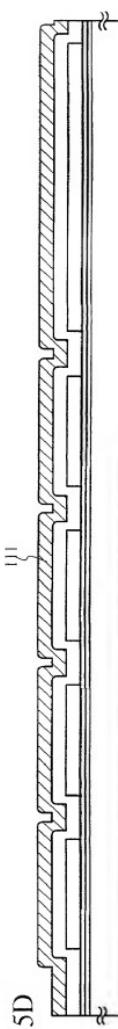


Fig.15D



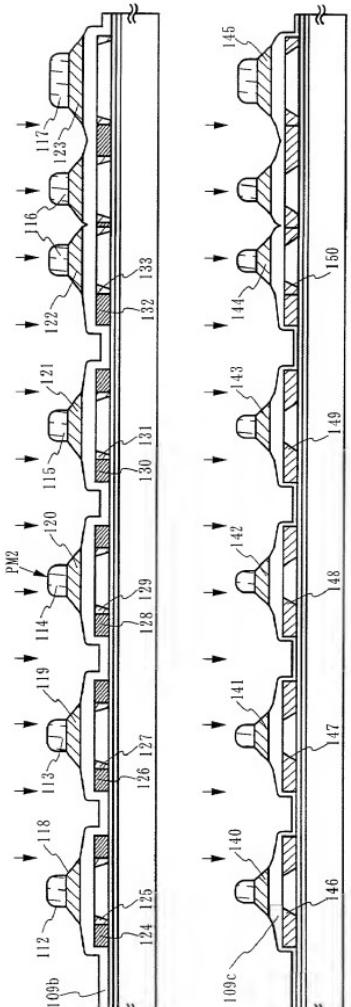


Fig. 16A

Fig. 16B

Fig. 16C

Fig.17A

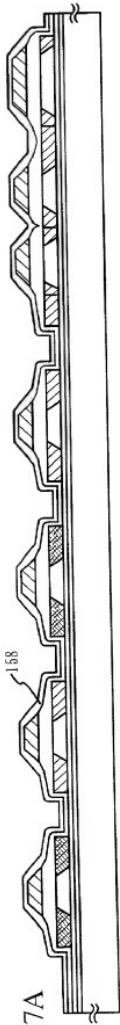
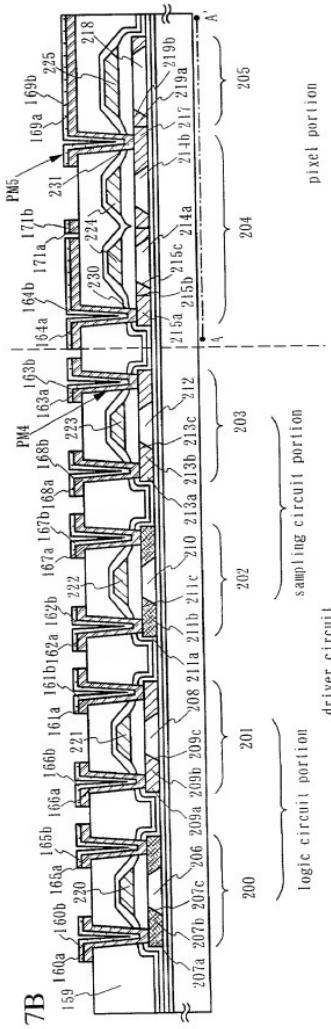


Fig.17B



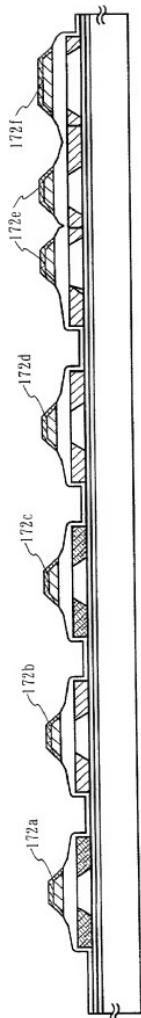


Fig. 18A

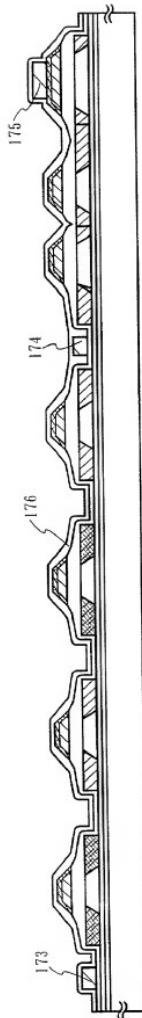


Fig. 18H

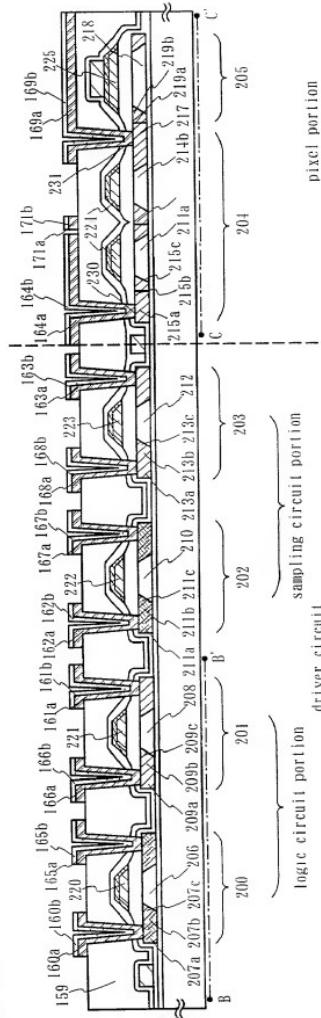


Fig. 18c

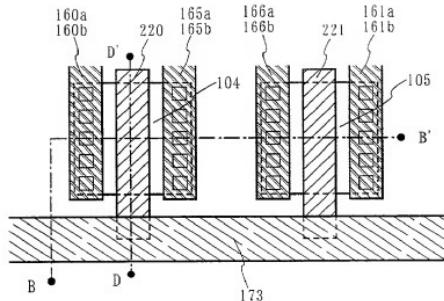


Fig.19A

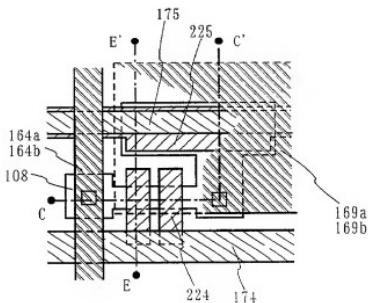


Fig.19B

Fig.20A

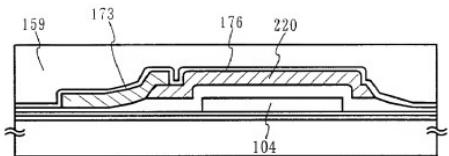
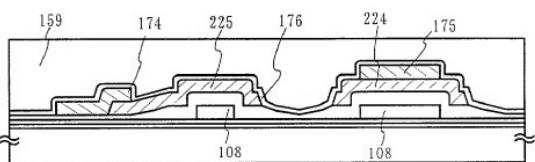


Fig.20B



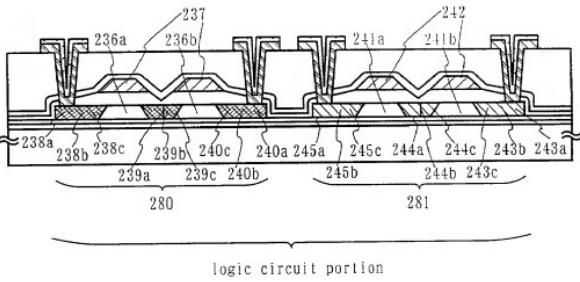
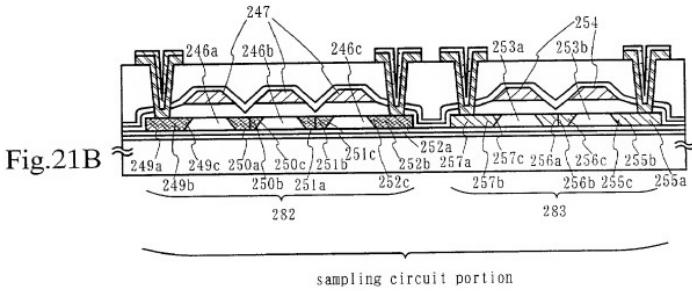


Fig.21A



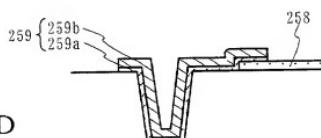
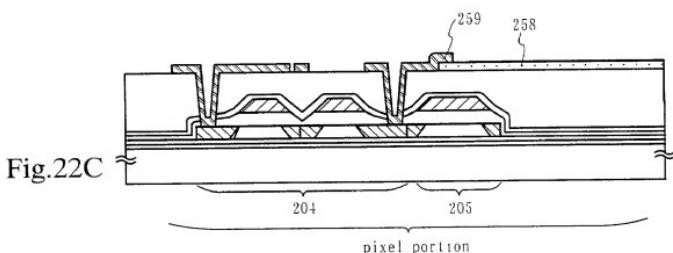
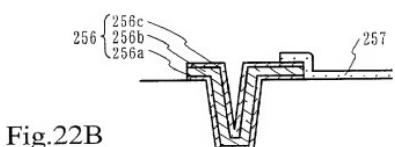
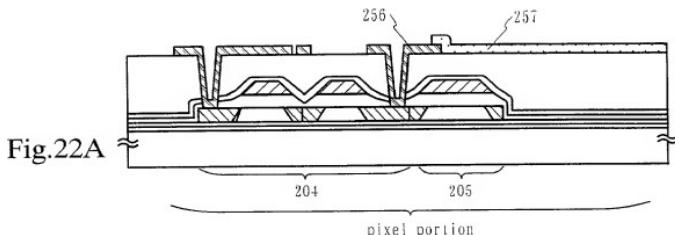


Fig.23

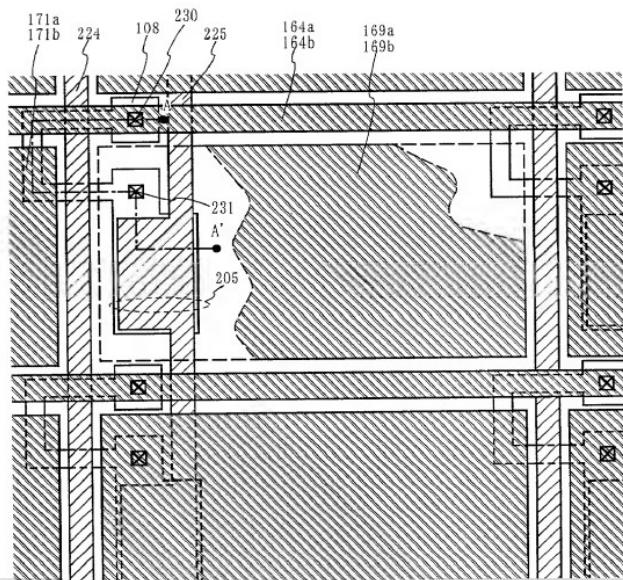


Fig. 24A

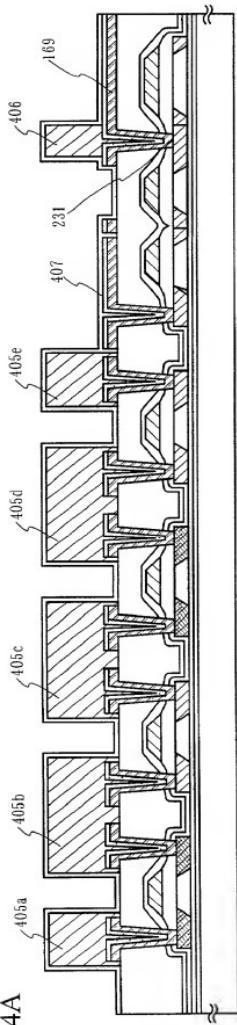
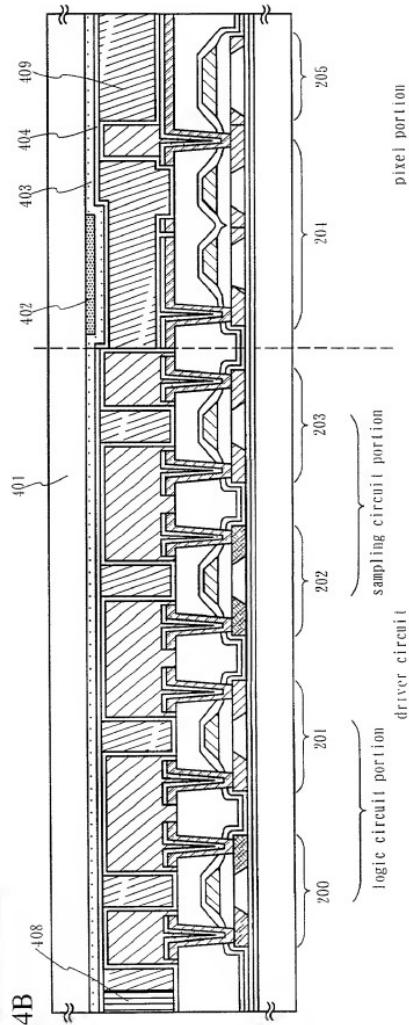


Fig. 24B



pixel portion

logic circuit portion  
driver circuit  
sampling circuit portion

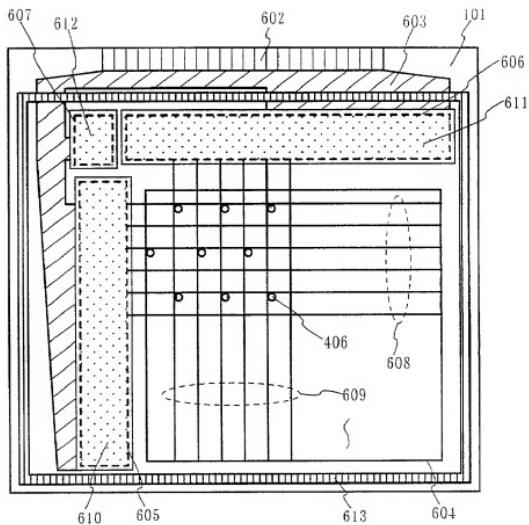


Fig.25

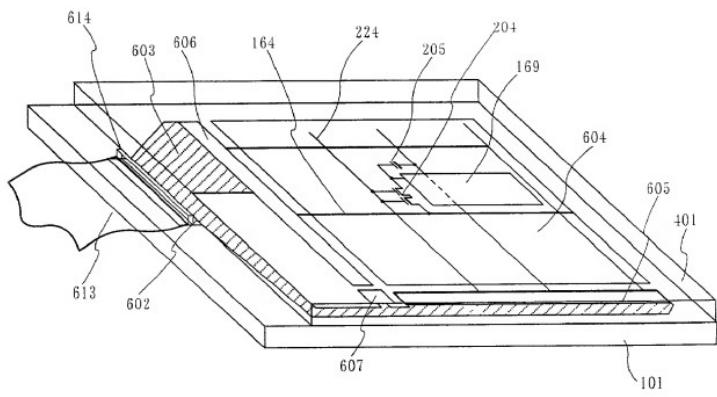


Fig.26

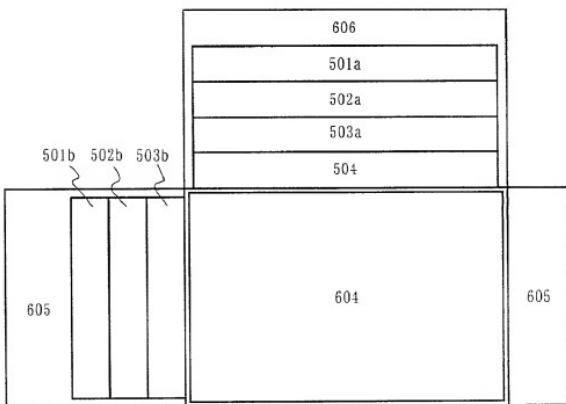


Fig.27

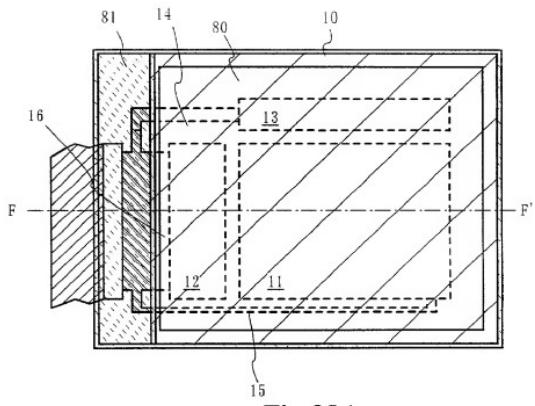


Fig.28A

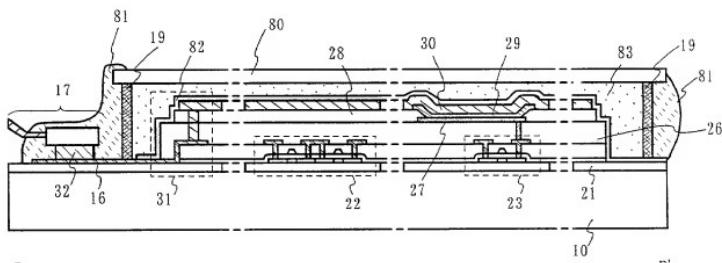


Fig.28B

வினாக்கள் மற்றும் தீர்வுகள்

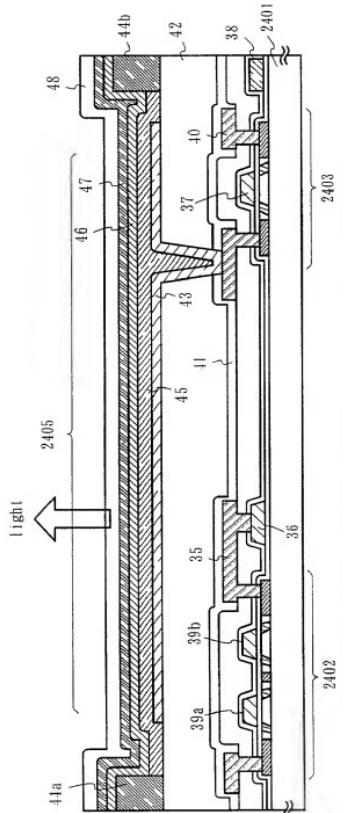


Fig.29A

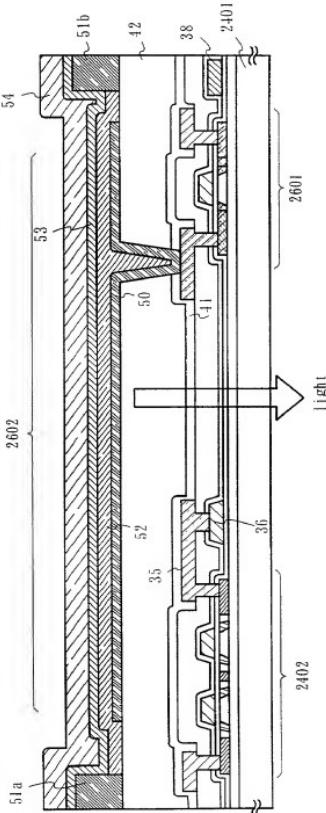


Fig.29B

Fig.30A

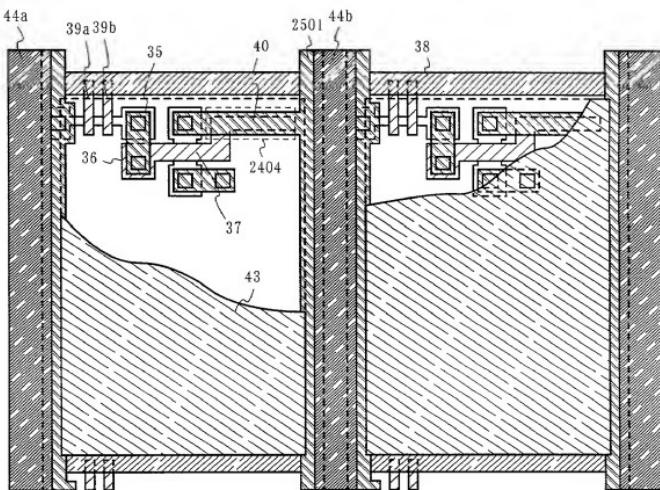
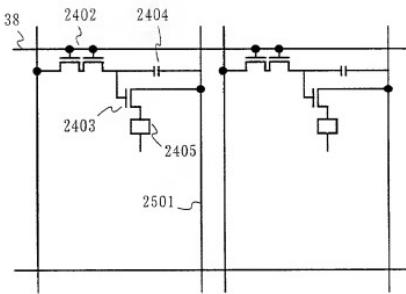


Fig.30B



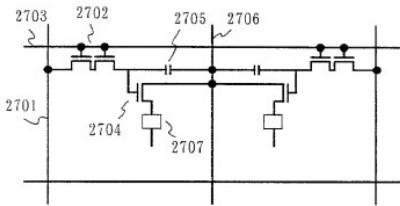


Fig.31A

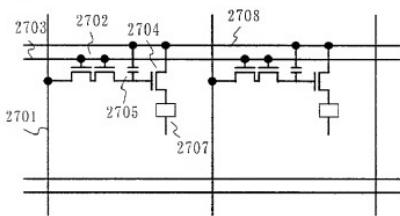


Fig.31B

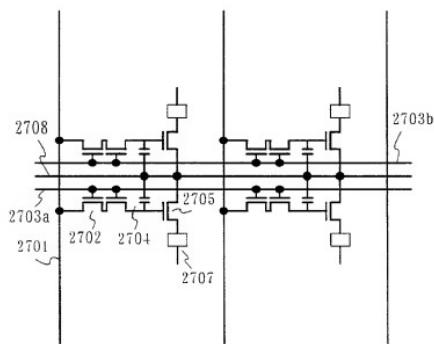


Fig.31C

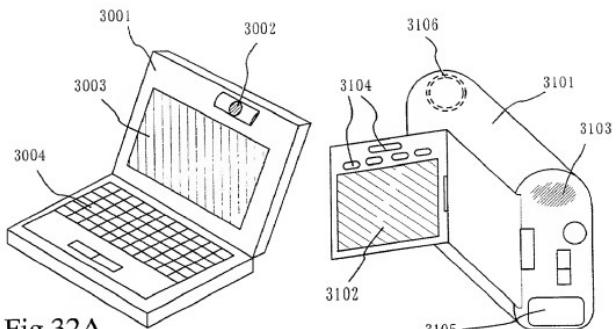


Fig.32A

Fig.32B

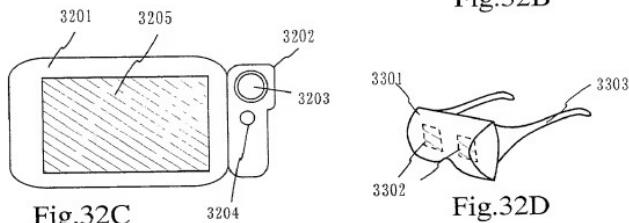


Fig.32C

Fig.32D

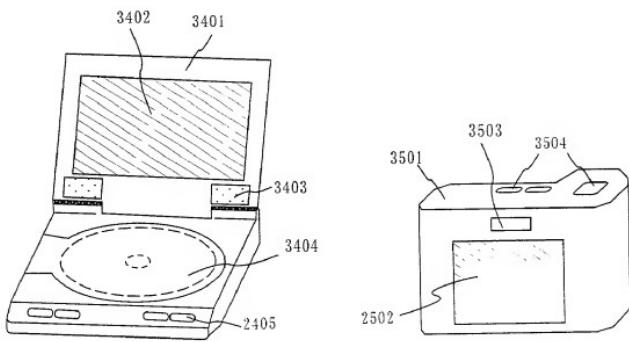


Fig.32E

Fig.32F

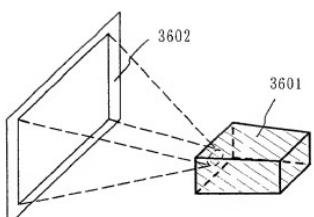


Fig.33A

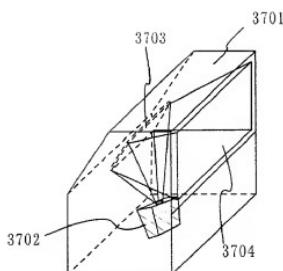


Fig.33B

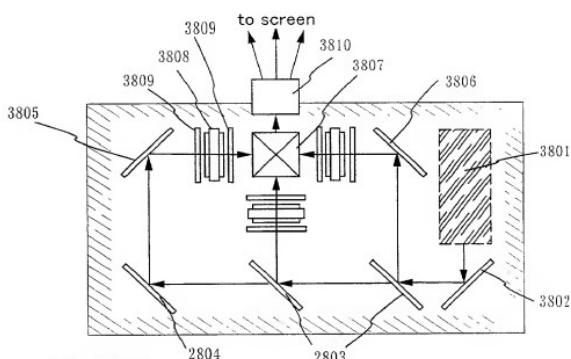


Fig.33C

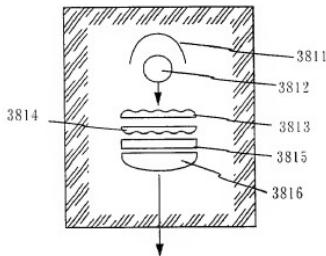


Fig.33D

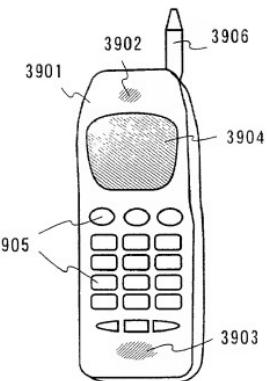


Fig.34A

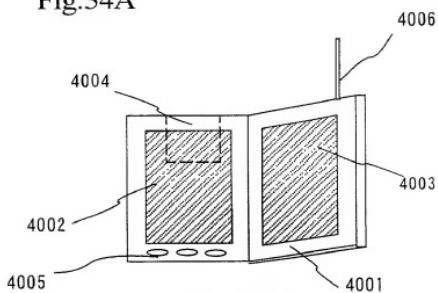


Fig.34B

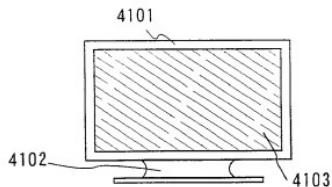


Fig.34C

Fig.35A

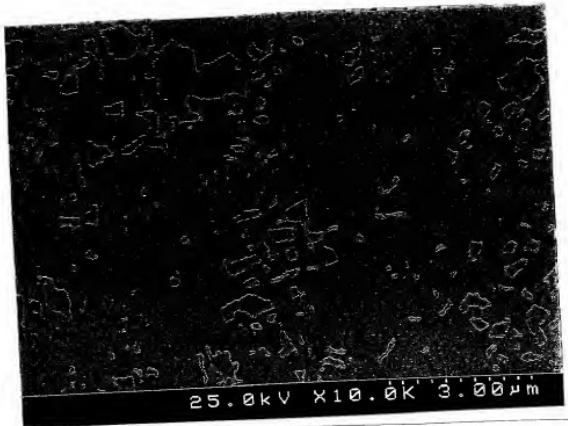


Fig.35B

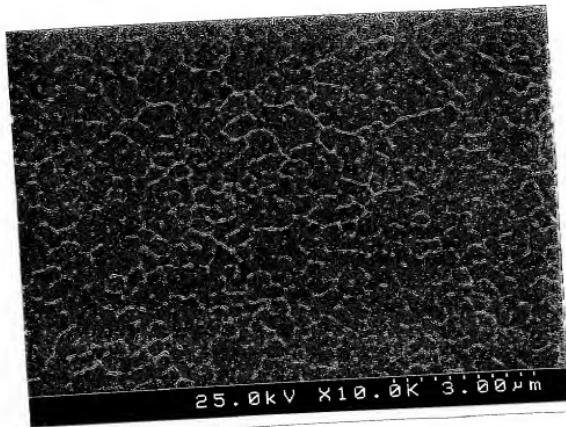
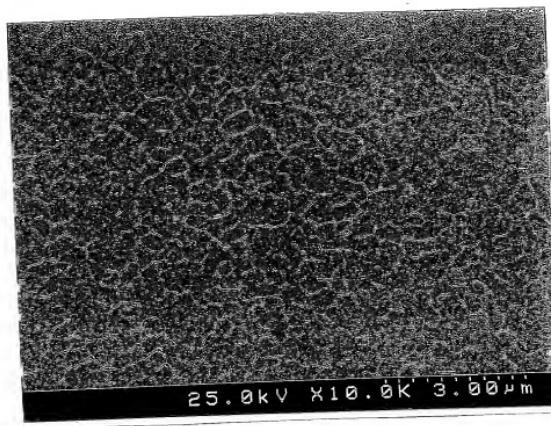


Fig.36A



25.0kV ×10.0K 3.00μm

Fig.36B



25.0kV ×10.0K 3.00μm